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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10067372	FILING DATE 02/07/2002	CLASS 714 368	SUBCLASS 2133	CAU 2859	EX. MINER
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****CONTINUING DATA VERIFIED:**
THIS APPLN CLAIMS BENEFIT OF 60/268,601 02/15/2001

**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	8167-Z
Verified and Acknowledged Examiners's initials		
TITLE : Multiple-capture DFT system for detecting or locating crossing clock-domain faults during self-test or scan-test		

U.S. DEPT. OF COMM. PAT. & TM. PTO-4361 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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